

Bitec HDCP IP Core Product Brief

Bitec HDCP IP Core

The Bitec HDCP IP Core is for use with the Bitec DP and HDMI IP cores. Its highly optimized design and meticulous security enables integration of content protection to FPGA and ASIC based designs.

All authentication and encryption is performed in logic without the need for high performance external processors. Also, as all computation is done within the core logic, no confidential values are accessible from outside the encrypted core. When the Bitec Key Management feature is used, the key data also becomes encrypted and can only be interpreted by the Bitec IP Core making a secure deployment of content protection applications.

Features

- Supports Version 1.3, 1.4 and 2.2 (CTS Compliant)
- 1-, 2- and 4-symbol/clock operation
- Optional Key Management Service*
- Fully self contained
 - Autonomous operation via FSM
 - Confidential values kept inside encrypted core
 - User managed keys or stored externally in encrypted form

Device Support

- Stratix 4 and 5
- Arria 2, 5 and 10
- ASIC and Structured ASIC available

Resource usage

		2-SYM/CLK	4-SYM/CLK
HDCP 1.4	SOURCE	2.5K ALMs	3.7K ALMs
	SINK	1.3K ALMs	2.8K ALMs
HDCP 2.2	SOURCE	11K ALMs	15K ALMs
	SINK	12K ALMs	18K ALMs

*For more information contact Bitec.

