

Bitec DSC IP Core Product Brief

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Display Stream Compression offers inter-operable, visually lossless, real-time video compression to satisfy the emerging high bandwidth and high resolution video technologies. Bitec offers a highly optimized implementation of the DSC 1.2a as a stand-alone component and/or integrated component to the Bitec DisplayPort 1.4 and HDMI 2.1 IP Cores

The DSC encoder or decoder has a rich set of parameterizations to enable an optimal customer implementation. User parameterized number of slices enables targeting of different transport protocols such as MIPI, DisplayPort or HDMI 2.1.

Features

- Supports Versions 1.1, 1.2 and 1.2a
- Optional support for Block Prediction
- Parameterized video geometry
- Supports RGB and YCbCr color spaces
- 4:4:4, 4:2:2 and 4:2:0 color spaces

Device Support

- ASIC, Structured ASIC and FPGA implementations are available

Resource usage : TBD

*For more information contact Bitec.

