



## Product Brief : HDMI 2.0b IP Core

Used in over 600 million devices, HDMI represents the most common interconnect standard for high definition video and audio.

The Bitec HDMI IP Core enables HDMI interconnectivity without the need for external HDMI ASSP devices. Supporting pixel clocks to 600Mhz, the IP core allows ULTRA HD designs while using minimal device i/o pin resources.

Both Video and Audio is supported with added low-level access to Auxiliary data packets. This feature allows low level development and testing of HDMI interfaces. It also allows a more flexible interface to the latest 3D and 4K video formats and control packets.

The IP Core has been interoperability tested at various CEA HDMI Plugfest events and is CTS Certified by official CEA test houses.



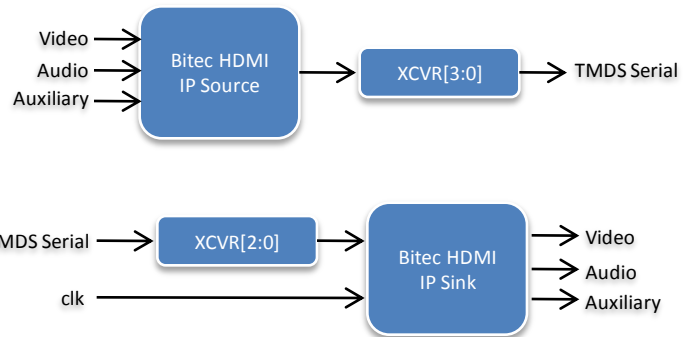
FMC HDMI  
Daughter card

### Features

- Support for Source and Sink HDMI 2.0b
- Video support for 8, 10, 12 and 16-bpc Deep Color operation
- Supports pixel clocks to 600Mhz (4k@60)
- Support for RGB and YCrCb color modes
- Supports UHD
- Accepts standard h/v/d input video format
- Compatible with DVI and Dual Link DVI
- Supports Audio to 32-channel, 3D & MST
- Supports single, double or quad pixel-per-clock
- Optional HDCP 1.4 and 2.2
- Optional CEC 1.4 and 2.0
- FPGA Evaluation platforms available

### Supported Technology

- MicroSemi PolarFIRE FPGA
- Lattice ECP5 FPGAs
- Altera GXB FPGAs
- ASIC



### Package includes

- Encrypted Core with unlimited use
- Reference designs
- 1-year update & support

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